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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	10508879
	Filing Date	2004-09-22
	First Named Inventor	Alexander Krasin
	Art Unit	2836
	Examiner Name	Zeev Kitov
	Attorney Docket Number	SC11823TC

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6664823	B2	2003-12-16	Yokoi	
	2	6903913	B2	2005-06-07	Ker et al	
	3	6198138	B1	2001-03-06	Hirota	
	4	5508649		1996-04-16	Shay	
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	6	5744842		1998-04-28	Ker	
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	9	5222146		1993-06-22	Bahl et al	
	10	5907464		1999-05-25	Maloney et al	
	11	6016070		2000-01-18	Uehara	
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	15	6392860	B1	2002-05-21	Lin et al	
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	20	6566715	B1	2003-05-20	Ker et al	
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	1	02/27795	WO	A2	2002-04-04	Li et al		<input type="checkbox"/>
	2	0851552	EP	A1	1998-07-01	Ravanelli et al		<input type="checkbox"/>

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	1	RAMIREZ-ANGULO et al; "Programmable BiCMOS Transconductor for Capacitor-Transconductor Filters"; 8030 Electronic Letters, 28(1992) No. 13, Stevenage, Herts., Great Britain	<input type="checkbox"/>
	2	MERRILL et al; "ESD Design Methodology"; ESD Symposium 93-233, pp. 5B.5.1-5B.5.5	<input type="checkbox"/>

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3	TORRES et al; "Modular, Portable, and Easily Simulated ESD Protection Networks for Advanced CMOS Technologies"	<input type="checkbox"/>
4	DABRAL et al; "Core Clamps for Low Voltage Technologies" ESD Symposium, pp. 3.6.1-3.6.9	<input type="checkbox"/>
5	ANDERSON et al; "Cross-Referenced ESD Protection for Power Supplies" ESD Symposium 98-86, pp. 2A.5.1-2A.5.10	<input type="checkbox"/>
6	WORLEY et al; "Sub-Micron Chip ESD Protection Schemes Which Avoid Avalanching Junctions" ESD Symposium 95-19, pp. 1.2.1-1.2.8	<input type="checkbox"/>

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**CERTIFICATION STATEMENT**

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

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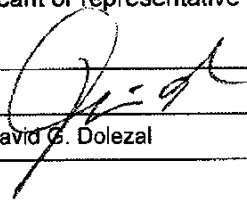
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- ☐ See attached certification statement.
- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature		Date (YYYY-MM-DD)	2006-07-10
Name/Print	David G. Dolezal	Registration Number	41,711

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